



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Christine Hau-Riege, et al.

Application No.: 10/697,214

Filed: October 29, 2003

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Group Art Unit: 2823

Examiner: William M. Brewster

Title: SEMICONDUCTOR COMPONENT AND METHOD FOR PRECLUDING  
STRESS-INDUCED VOID FORMATION IN THE SEMICONDUCTOR  
COMPONENT

**AMENDMENT UNDER 37 C.F.R. § 1.312**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Madam/Sir:

In response to the Notice of Allowance and Issue Fee Due mailed April 7, 2005,  
please amend the application as follows: